XA-9519A PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Hidenori SATO et al.

Appln. No.:

Filed: Herewith

For: MANUFACTURING METHOD OF SEMICONDUCTOR INTEGRATED

CIRCUIT DEVICE

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

Applicants wish to make of record the documents cited in prior Application No. 09/905,887 filed July 17, 2001, whether cited by Applicants or by the Patent Office. The documents are listed on the attached Form PTO-1449.

Respectfully submitted,

Mitchell W. Shapiro

Reg. No. 31,568

MWS: jab

Miles & Stockbridge P.C. 1751 Pinnacle Drive, Suite 500 McLean, Virginia 22102-3833 (703) 903-9000 August 19, 2003

FORM PTO-1449					Atty. Docket No.		Appl	n. No.
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LIST OF DOCUMENTS CITED BY APPLICANT					Applicant			
					Hidenori SATO et al.			
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form with next communication to Applicant.